

## Description

# NONLINEAR OVERLAP METHOD FOR TIME SCALING

### BACKGROUND OF INVENTION

[0001] 1. Field of the Invention

[0002] The present invention relates to a signal-synthesizing method, and more particularly, to a nonlinear overlap method for time scaling.

[0003] 2. Description of the Prior Art

[0004] Due to the dramatic progress in electronic technologies, an AV player such as a Karaoke can provide more and more amazing functions, such as audio clean-up, dynamic repositioning of enhanced audio and music (DREAM), and time scaling. Time scaling (also called time stretching, time compression/expansion, or time correction) is a function to elongate or shorten an audio signal while keeping the pitch of the audio signal approximately unchanged. In short, time scaling only adjusts the tempo of

an audio signal.

[0005] In general, an AV player performs time scaling with one of the three following methods: Phase Vocoder, Minimum Perceived Loss Time Expansion/Compression (MPEX), and Time Domain Harmonic Scaling (TDHS). Phase Vocoder transforms an audio signal into a complex Fourier representation signal with Short Time Fourier Transform (STFT) and further transforms the complex Fourier representation signal back to a time scaled audio signal corresponding to the original audio signal with interpolation techniques and iSTFT (inverse STFT). MPEX is a method researched and developed by Prosoniq for simulating characteristics of human hearing, similar to an artificial neural network. MPEX records audio signals received for a predetermined period and tries to "learn" the audio signals, so as to either elongate or shorten the audio signals. TDHS is one of the most popular methods for time scaling. TDHS first establishes an autocorrelogram of a first audio signal, the autocorrelogram consisting of a plurality of magnitudes, and then delays the first audio signal by a maximum index corresponding to a maximum magnitude, a largest magnitude among all of the magnitudes of the autocorrelogram, to form a second audio signal, and lastly

synchronizes and overlap-adds (SOLA) the first audio signal to the second audio signal to form a third audio signal longer than the first audio signal.

[0006] In a computer system, the autocorrelogram is usually established by a digital signal processing (DSP) chip designed to manage complex mathematic calculation such as convolution and fast Fourier transform (FFT). However, a process by the DSP chip to synthesize the third audio signal from the first and second audio signals is tedious and sometimes unnecessary.

#### SUMMARY OF INVENTION

[0007] It is therefore a primary objective of the claimed invention to provide a nonlinear overlap method for time scaling to efficiently synthesize a third audio signal from a first audio signal and a second audio signal without sacrificing the quality of the third audio signal dramatically.

[0008] According to the claimed invention, the nonlinear overlap method for time scaling to synthesize an  $S_3[n]$  signal from an  $S_1[n]$  signal and an  $S_2[n]$  signal, the  $S_1[n]$  signal having  $N_1$  elements and the  $S_2[n]$  signal having  $N_2$  elements, comprises:

[0009] (a) delaying the  $S_2[n]$  signal by a predetermined number of elements and forming an  $S_5[n]$  signal;

- [0010] (b) establishing a cross-correlogram of a cross-correlation function of the  $S_1[n]$  signal and the  $S_5[n]$  signal, the cross-correlogram including a plurality of magnitudes, each of the magnitudes corresponding to an index; and
- [0011] (c) setting the  $S_3[n]$  signal as values of the elements of:
- [0012]  $S_1[n]$ , where  $0 \leq n < (\text{the predetermined number} + \text{a first threshold value} + \text{a maximum index})$ , the maximum index corresponding to a largest magnitude among all of the magnitudes of the cross correlogram;
- [0013]  $S_1[n]$  weights and adds to an  $S_4[n]$  signal that lags the  $S_5[n]$  signal by the maximum index, where  $(\text{the predetermined number} + \text{the first threshold value} + \text{the maximum index}) \leq n < (N_1 - \text{a second threshold value})$ ; and
- [0014]  $S_4[n (\text{the predetermined number} + \text{the maximum index})]$ , where  $(N_1 - \text{the second threshold value}) \leq n \leq (N_2 + \text{the predetermined number} + \text{the maximum index})$ ;
- [0015] wherein the first and second threshold values are not equal to zero at the same time.
- [0016] It is an advantage of the claimed invention that the method calculates values between the first threshold and the second threshold instead of all values of the overlapped signal from A to Z to save time for a DSP chip to synthesize the  $S_3[n]$  signal from the  $S_1[n]$  and  $S_2[n]$  signals

and promote a computer where the DSP chip is installed in.

[0017] These and other objectives of the claimed invention will no doubt become obvious to those of ordinary skill in the art after reading the following detailed description of the preferred embodiment that is illustrated in the various figures and drawings.

#### **BRIEF DESCRIPTION OF DRAWINGS**

[0018] Fig.1 is a flow chart of a method according to the present invention.

[0019] Fig.2 is a schematic diagram demonstrating how the method synthesizes an  $S_3[n]$  signal from an  $S_1[n]$  signal and an  $S_2[n]$  signal according to the present invention.

[0020] Fig.3 is a schematic diagram demonstrating how the method elongates an audio signal according to the present invention.

[0021] Fig.4 is a schematic diagram demonstrating how the method shortens an audio signal according to the present invention.

#### **DETAILED DESCRIPTION**

[0022] After establishing an autocorrelogram corresponding to a first audio signal and a second audio signal (or a signal

lagging the first audio signal by a predetermined number), the autocorrelogram consisting of a plurality of magnitudes, a method 100 of the preferred embodiment of the present invention determines a maximum index corresponding to a maximum magnitude, a largest magnitude in the autocorrelogram, and calculates a third audio signal according to the first audio signal, the second audio signal, the maximum index, a first threshold and a second threshold. In detail, in order to save time for a digital signal processing (DSP) chip to synthesize the third audio signal from the first and second audio signals, the method 100, having determined the maximum index and delaying the second audio signal by the maximum index, does not weight and add all of an overlapped signal mixed with the first audio signal and the second audio signal as well to the second audio signal but weights and adds part (a region between the first threshold and the second threshold) of the overlapped signal to the second audio signal instead and forms the third audio signal.

[0023] Please refer to Fig.1, which is a flow chart of a method 100 of the preferred embodiment according to the present invention. The method 100 comprises the following steps:

[0024] Step 102:Start;

[0025] (An  $S_3[n]$  signal is to be synthesized from an  $S_1[n]$  signal and an  $S_2[n]$  signal. For simplicity, the  $S_1[n]$  signal and  $S_2[n]$  signals are defined to contain  $N_1$  and  $N_2$  signals respectively.)

[0026] Step 104:Delaying the  $S_2[n]$  signal by a predetermined number  $\Delta$  and forming an  $S_5[n]$  signal;

[0027] (In order to prevent run-in from occurring in a process a pickup of an A/V player reads the  $S_3[n]$  signal, the method 100 delays the  $S_2[n]$  signal by the predetermined number  $\Delta$  and then determines an maximum index

$\tau_{\max}$

crucial for the process to synthesize the  $S_3[n]$  signal from the  $S_1[n]$  signal and the  $S_2[n]$  signal. In the preferred embodiment, the predetermined number  $\Delta$  is equal to  $[N/3]$ .)

[0028] Step 106: Establishing an autocorrelogram of the  $S_1[n]$  and  $S_5[n]$  signals and delaying the  $S_5[n]$  signal to form an  $S_4[n]$  signal according to the maximum index

$$\tau_{\max}$$

corresponding to a maximum magnitude in the autocorrelogram;

[0029] (The autocorrelogram comprises a plurality of magnitudes of a cross-correlation function, each of the magnitudes corresponding to a distinct index.)

[0030] Step 108: Synthesizing the  $S_3[n]$  signal from the  $S_1[n]$  sig-



nal and the  $S_4[n]$  signal;

[0031] (The  $S_3[n]$  signal is equal to

[0032] the  $S_1[n]$  signal, where  $0 \leq n < (\text{the predetermined number } \Delta + \text{a first threshold value } th_1 + \text{the maximum index}$

$\tau_{\max}$

);

[0033] the  $S_1[n]$  signal weights and adds to the  $S_4[n]$  signal, where (the predetermined number  $\Delta +$  the first threshold value  $th_1 +$  the maximum index

$\tau_{\max}$

[0034] )  $\leq n < (N_1$  a second threshold value  $th_2$ ); and  
the  $S_4[n]$  (the predetermined number  $\Delta$  + the maximum  
index

$\tau_{\max}$

)] signal, where ( $N_1$  - the second threshold value  $th_2$ )  $\leq n$   
 $\leq (N_2 + \text{the predetermined number } \Delta + \text{the maximum}$   
index

$\tau_{\max}$

);

[0035] wherein the first threshold value  $th_1$  and second threshold value  $th_2$  are not equal to zero at the same time.)

[0036] Step 110:End.

[0037] Please refer to Fig.2, which is a schematic diagram demonstrating how the method 100 synthesizes the  $S_3[n]$  signal from the  $S_1[n]$  and  $S_2[n]$  signals according to the present invention. In Fig.2, a first part 401 shows the  $S_1[n]$  and  $S_2[n]$  signals in the step 102 of the method 100, a second part 402 shows the  $S_1[n]$  and  $S_5[n]$  signals calculated from the step 104 of the method 100, a third part

403 shows the maximum index

$$\tau_{\max}$$

and the  $S_4[n]$  signal calculated from the step 106 of the method 100, a fourth part 404 and a fifth part 405 the  $S_3[n]$  signal synthesized from the  $S_1[n]$  and the  $S_4[n]$  signals in the step 108 of the method 100.

[0038] The  $S_3[n]$  signal shown in the fourth part 404 of Fig.2 is equal to

$$\frac{(N_1 - th_2 - n)}{(N_1 - (\Delta + \tau_{\max} + th_1 + th_2))} * S_1[n] + \frac{n - (\Delta + th_1 + \tau_{\max})}{(N_1 - (\Delta + \tau_{\max} + th_1 + th_2))} * S_4[n - (\Delta + \tau_{\max}$$

, where (the predetermined number  $\Delta$  + the maximum index

$\tau_{\max}$

+ the first threshold value  $th_1$ )  $\leq n < (N_1$  the second threshold value  $th_2$ ).

[0039] The  $S_3[n]$  signal shown in the fourth part 405 of Fig.2 is equal to

$$\frac{(N_1 - n)}{(N_1 - (\Delta + \tau_{\max}))} * S_1[n] + \frac{n - (\Delta + \tau_{\max})}{(N_1 - (\Delta + \tau_{\max}))} * S_4[n - (\Delta + \tau_{\max})]$$

, where (the predetermined number  $\Delta$  + the maximum index

$\tau_{\max}$

+ the first threshold value  $th_1$ )  $\leq n < (N_1$  the second threshold value  $th_2$ ).

[0040] If the  $S_1[n]$  signal is the same as the  $S_2[n]$  signal and both are derived from the  $S[n]$  at an identical region, as shown in Fig.3, the method 100 in fact elongates the  $S_1[n]$ . On the contrary, if the  $S_1[n]$  signal and the  $S_2[n]$  signals are different from each other and are derived from the  $S[n]$  at two distinct regions respectively, as shown in Fig.4, the method 100 in fact shortens the  $S_1[n]$ , an  $S_6[n]$  (discarded) and the  $S_2[n]$  signals into the  $S_3[n]$  signal.

[0041] In contrast to the prior art, the present invention can provide a method to synthesize the  $S_3[n]$  signal from the  $S_1[n]$



and  $S_2[n]$  signals based on the maximum index corresponding to the maximum magnitude of the autocorrelation and the first and second threshold values for confining the overlapped signal simultaneously mixed with the  $S_1[n]$  and the  $S_2[n]$  signals. Instead of calculating all values of the overlapped signal from A to Z, the method calculates values between the first threshold and the second threshold to save time for a DSP chip to synthesize the  $S_3[n]$  signal from the  $S_1[n]$  and  $S_2[n]$  signals and promote a computer where the DSP chip is installed in.

[0042] Following the detailed description of the present invention above, those skilled in the art will readily observe that numerous modifications and alterations of the device may be made while retaining the teachings of the invention. Accordingly, the above disclosure should be construed as limited only by the metes and bounds of the appended claims.